Docket No.

242358US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Kiyoshi HAYASE

SERIAL NO: NEW APPLICATION

GAU:

FILED:

HEREWITH

EXAMINER:

FOR:

MULTIPROCESSOR SYSTEM CAPABLE OF EFFICIENTLY DEBUGGING PROCESSORS

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☐ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment form is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Marvin J. Spivak

Registration No. 24,913

C. Irvin McClelland Registration Number 21,124

Customer Number

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03)

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22850

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03) DOCKET NO: 242358US2

Sheet __1_ of __1_

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SERIAL NO: New Application

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FOR: MULTIPROCESSOR SYSTEM CAPABLE OF EFFICIENTLY DEBUGGING

PROCESSORS

STATEMENT OF RELEVANCY

Reference AW on Form PTO- 1449:

"JTAG debugging of a diversified CPU", M. Yura, Design Wave Magazine, February 2000, CQ Publishing Co., Ltd., pp. 38-45.

According to this document, in case plural processors are built in one silicone, each processor is to mount an interface for debugging. In this case, a scanning chain becomes long, and response of debugging and downloading take substantial amount of time.

Form PTO 1449 U.S. DEPARTMENT OF COMMERCE (Modified) PATENT AND TRADEMARK OFFICE				ATTY DOCKET NO. 242358US2		SERIAL NO. NEW APPLICATION		
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LIST OF	REFER	RENCES CITED BY AP	PLICANT	Kiyoshi HAYASE				
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U.S. PATENT DOCUMENTS								
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	AO	2002-73363	03/12/02	Japan (with English extract)				x
	AP	2002-24201	01/25/02	Japan (with English extract)				×
	AQ	2001-202269	07/27/01	Japan (with English extract)				×
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)								
	M. YURA, "JTAG Debugging of a Diversified CPU", DESIGN WAVE MAGAZINE, February 2000, CQ Publishing Co., Ltd., pgs. 38-45							
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	AZ				Additional References sheet(s) attached			
Examiner					Date Co	Date Considered		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								